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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/725,981	12/02/2003	Zane Drussel	150.00620104	4931
26813	7590 06/24/2004		EXAMINER	
MUETING,	RAASCH & GEBHAI	TRINH, N	TRINH, MINH N	
P.O. BOX 581415 MINNEAPOLIS, MN 55458			ART UNIT	PAPER NUMBER
			3729	

DATE MAILED: 06/24/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

•		Application No.	Applicant(s)		
		10/725,981	DRUSSEL ET AL.		
	Office Action Summary	Examiner	Art Unit		
		Minh Trinh	3729		
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the c	orrespondence address		
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period we are to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be timed within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).		
Status					
·	Responsive to communication(s) filed on <u>02 Deservice</u> This action is FINAL . 2b) This Since this application is in condition for allower closed in accordance with the practice under Expression in the Expression in the practice under Expression in the practice un	action is non-final. nce except for formal matters, pro			
Disposit	ion of Claims				
5)□ 6)⊠ 7)□	Claim(s) <u>63-68</u> is/are pending in the application 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) <u>63-68</u> is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.	•		
Applicat	ion Papers				
10)	The specification is objected to by the Examiner The drawing(s) filed on is/are: a) acce Applicant may not request that any objection to the o Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex-	epted or b) objected to by the Edrawing(s) be held in abeyance. See on is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).		
Priority ι	ınder 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
2) 🔲 Notic 3) 🔯 Infor	t(s) te of References Cited (PTO-892) te of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 3/1/04.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa			

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DETAILED ACTION

Priority

1. Since the related application *SN*: 09/865,336 has already matured into a patent. Therefore,"(pending)" (see specification page 1, line 2) should be changed to: --now Patent No. 6,664,480, issued December 16, 2003--.

The title

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed. The following title is suggested: "PCB Singulation Method " or the like.

The abstract

3. The abstract of the invention should be modified in order to reflect the method invention.

Double Patenting

4. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

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Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

5. Claims 63-65 and 66-68 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-16 of U.S. Patent No. 6,047,470 to Drussel et al, hereinafter '470. Although the conflicting claims are not identical, they are not patentably distinct from each other because the subject matter claimed in the instant application is fully claimed in the patent and is covered by the patent since the patent and the application are claiming common subject matter, as follows:

The '470 claim a singulation method as recited in details in claims 63-68 of the present application comprising: providing a circuit board substrate assembly comprising a substrate material having first and second opposed edges, the substrate material comprising: a plurality of circuit forming regions comprising at least one pair of adjacent circuit forming regions, a first interconnection region and a second interconnection region, wherein the first interconnection region extends along the first edge and is located between the first edge and the plurality of circuit forming regions, wherein the second interconnection region extends along the second edge and is located between the second edge and the plurality of circuit forming regions (compare claim 12, lines 1-15), and

at least one opening defined in the substrate material between each pair of adjacent circuit forming regions, wherein the at least one opening extends into at least

portions of both the first interconnection region and second interconnection regions (compare claim 12, lines 16-21),

removing at least a portion of the first and second interconnection regions along a respective first and second singulation axis defined therein parallel to one another to singulate at least two circuit forming regions of the plurality of circuit forming regions, the first singulation axis and the second singulation axis intersecting with the at least one opening defined between each pair of adjacent circuit forming regions (compare claim 12, lines 22-30), etc.

Regarding claims 64-65, the '470 claim every aspect limitations as recited in these claims (compare claims 13 and 14 at col. 11, lines 9-18).

Regarding claims 66-68, the '470 claim the limitations of claims 66-68 (compare claim 16, at col. 12, lines 1-20).

Prior Art References

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Prior art references are cited for their teaching of PCB manufacturing and singulating method or the like.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Minh Trinh whose telephone number is (703) 305-2887. The examiner can normally be reached on Monday -Thursday 8:00 am to 4:30 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Peter Vo can be reached on (703) 308-1789. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Minh Trinh 6/22/04

Patent Examiner Group 3729

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